## **Course Syllabus**

## **Facility layout**

Introduction

Methods and practices

Services Layout

## **Facility Layout optimization**

ALDEP, CORELAP, CRAFT
Genetic Algorithm
Artificial Bee Colony algorithm

## **Process Optimization Lab.**

Hands on product realization
Process Simulation
ARENA Simulation
Technomatix Practices
Digital Manufacturing &
process planning

Dates: 26.09.2014 TO 28.09.2014 Fees: Rs. 7,500 (for students) Rs. 10,500 (for others) Venue: IME Department, IIT

Kanpur.





# Objective:

With continuous changing environment in manufacturing, along with on-going globalization, process optimization is a key factor in the success of a company. Money can be lost daily through poor process optimization(inefficient schedules, local instead of global optimization, inefficient resource allocation, and poor productivity and improper facility layout.

For information and registration, please contact:

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# FACILITY LAYOUT AND PROCESS OPTIMIZATION

Dr.Deepu Philip & Team IIT Kanpur

### **A CDTE Course\***

on

## Facility Layout and Process Optimization

September 26–28, 2014

(Registration form should contain the following information. It should be printed (not hand written) on A4 size paper)

Name:

Position:					
Department:					
Institution/Organ	ization:				
Address:					
E-mail Address:	Mobile No.:				
Telephone No.:	Fax No.:				
•					
<b>Educational Back</b>	kground:				
Degree	Field of		% marks/		Rank in the
	Specialization	Institution	CGPA/CPI	Year	class
B.E./B.Tech	•		•		
M.E./M.Tech.					
MBA					
B.Sc/ B.Com					
M.Sc/M.Com					
Ph.D.					
Other					
Other					
Areas of Researc	h Interest:				
		ocess Optimization " at IITK or elsewhere: Yes / No			
	e details				
(11 ) 00) produce 811		···)			
Payment details					
		Process Optimization", payable at IIT Kanpur)	dated		
	drawn at				

 $Signature\ of\ applicant$